Mr. N. Kishore Narang
Mentor, Principal Design Strategist & Architect
Narnix Technolabs

Kishor is Technology Advisor, Mentor, Design Strategist & Architect in Electrical, Electronics & ICT with over 45 years of professional experience in education, research, design and consulting. Over 30 years of hardcore Research and Design Development Experience in Solutions, Systems, Products, Hardware, Software & Firmware (Embedded Software) across diverse technology & application domains, and over 10 years of Consultancy Experience to different segments of business & industry. He has over 250 Research & Design Mentees in the Electronics, ICT & STI Ecosystems. He is Research Advisor, Institute of Informatics and Communication, University of Delhi; Member, Academic Council, D Y Patil International University, Pune; & Member, Academic Committee, Electronics & ICT Academy at National Institute of Technology, Patna. Professionally, he is an Electronics Design Engineer practicing design & development of a wide spectrum of Products, Systems & Solutions as an Independent Design House – NARNIX since 1981.

Mentoring many Deep Tech & Disruptive Tech Startups.

For last 10 years, he has been deeply involved in standardization in the electrical, electronics, communications, information technology and cyber security domains with a focus on identifying gaps in standards to bring harmonization through standardized interfaces to ensure end-to-end Interoperability. He has been leading national standardization initiatives at BIS, the Indian national standards development organization (SDO), in smart cities, smart manufacturing, smart energy, and active assisted living as the founding Chairman of the Smart Infrastructure Sectional Committee LITD 28 (15+ standards on Unified Digital Infrastructure already published), along with contributing to multiple other SDOs and initiatives. Globally, he is Vice Chair-Strategy and Project Leader of two international standards in IEC SyC Smart Cities – SCRAM: Smart Cities Reference Architecture Methodology IEC 63188 (to be published in July 2022) and SCRA: Smart Cities Reference Architecture IEC 63205 being developed jointly with ISO TC 268. Chair, IEEE SA Working Group P2784 – Smart Cities

Technology Framework. Convenor of Communication Technologies Work Group in IEC SyC Communication Technologies & Architectures, Member, IEC SMB/SG 12 -Digital Transformation and Systems Approach, Member, IEC MSB/SWG 14 - SMART Standards – An Market and Industry perspective; Co-Editor in ISO/IEC JTC1/WG11 Four Standards, and Chair of Advancing Research Work Group & member Steering Committee of OCEANIS, Member UL STP 3600 - 'Measuring and Reporting Circular Economy Aspects of Products, Sites and Organizations', beyond proactive contributions in many committees in global SDOs. He is the Standards Column Editor in **IEEE IoT Magazine**.

After developing Unified Digital Infrastructure ICT Reference Architecture in BIS, currently, he is developing a Comprehensive "5G inclusive ICT Architecture & Cyber Security Reference Architecture for Critical Infrastructure" and the 5G Application Layer Standards development in India. Globally, he is leading TRUSTWORTHINESS Reference Architecture in ISO/IEC JTC1/WG 13.

He is mentoring IEEMA Smart Grid Division to develop a comprehensive Reference Architecture for Indian Electricity Infrastructure followed by Cyber Security Architecture, Standards Mapping and Compliance Testing framework.